Abstract Of The Disclosure

A control circuit for controlling an electronic circuit, which has a current path through a semiconductor switch and a line; when the semiconductor switch is switched, the inductance of the line and/or of a component in the current path producing an excess voltage between a first and a second current-carrying terminal of the semiconductor switch; the control circuit having a controllable current source for charging or discharging a charge-controlled gate of the semiconductor switch with the aid of a control current, as well as a control unit; the control unit controlling the current source in such a manner, that in the case of a switching operation, the terminal voltage across the current-carrying terminals of the semiconductor switch does not exceed a predefined setpoint terminal voltage.

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